

03CO 0400 08-14-01

PATENT

Attorney Docket No. 13200-00072

#4

IDS w/Refers

Applicant: Pierte Roo, et al.

Serial No.: 09/920,240

Filed: August 1, 2001

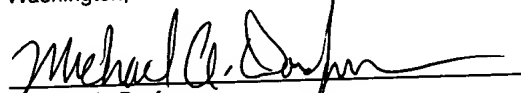
Title: ACTIVE RESISTIVE SUMMER  
FOR A TRANSFORMER HYBRID

Group Art Unit: Not yet known

Examiner: Not yet known

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as first-class mail, postage pre-paid, in an envelope addressed to: Assistant Commissioner for Patents Washington, D.C. 20231 on August 23, 2001.

  
Michael A. Dorfman  
Registration No. 46,669  
Attorney for Applicant

Assistant Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT


Dear Sir:

Pursuant to the provisions of 37 C.F.R. §§ 1.56, 1.97, and 1.98, the documents listed on the attached Form PTO-1449 are submitted herewith for consideration during the prosecution of the above-identified patent application. This document is being filed in accordance with 37 C.F.R. § 1.97(b) and, to the Applicant's knowledge, prior to the mailing of a first Official Action. Accordingly, Applicant respectfully requests that the enclosed documents be considered and made of record in the application.

The Commissioner is hereby authorized to charge any additional fees which may be required with respect to this communication or credit any overpayment to Deposit Account No. 50-1214.

Date: August 23, 2001

Respectfully submitted,

  
Michael A. Dorfman  
Registration No. 46,669

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Form PTO-1449

**INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION**

(Use several sheets if necessary)

Docket Number  
13200-00072Application Number  
09/920,240Applicant  
Pierte Roo, et al.Filing Date  
August 1, 2001Group Art Unit  
Not yet known**U. S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROP.
	R	E	3	0	1	1	1	10/09/79	Blood, Jr.	178	58 R	
	4	1	1	2	2	5	3	09/05/78	Wilhelm	178	58 R	
	4	1	5	2	5	4	1	05/01/79	Yuen	178	59	
	4	3	9	3	4	9	4	07/12/83	Belforte et al.	370	27	
	5	3	0	7	4	0	5	04/26/94	Sih	379	410	
	5	3	8	8	0	9	2	02/07/95	Koyama et al.	370	32.1	
	5	5	1	7	4	3	5	05/14/96	Sugiyama	364	724.19	
	5	5	3	9	7	7	3	07/23/96	Knee et al.	375	232	
	5	6	5	9	6	0	9	08/19/97	Koizumi et al.	379	410	
	5	6	6	6	3	5	4	09/09/97	Cecchi et al.	370	284	
	5	7	9	6	7	2	5	08/18/98	Muraoka	370	290	
	5	8	2	2	4	2	6	10/13/98	Rasmus et al.	379	402	
	5	8	2	5	8	1	9	10/20/98	Cogburn	375	257	
	5	8	6	4	5	8	7	01/26/99	Hunt	375	316	

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	Translation	
						Yes	No
	WO 99/46867	09/16/99	PCT				

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

	"Microelectronic Circuits," Third Edition, Sedra et al., Chapter 2: Operational Amplifiers, §2.4, pp. 61-63, 1991.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.